



10-14-03

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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application of: Amin et al.

Confirmation No.: 2154

Serial No.: 09/839,637

Art Unit: 2822

Filed: April 20, 2001

Examiner: To be assigned

For: QUANTUM BIT WITH A MULTI-  
TERMINAL JUNCTION AND LOOP  
WITH A PHASE SHIFT

Attorney Docket No: 11090-033-999  
(formerly M-8915US)

Office of Initial Patent Examination  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

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**REQUEST FOR CORRECTED FILING RECEIPT**

Sir:

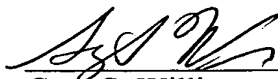
Applicants herewith request that the filing receipt be corrected to delete a priority claim to United States Provisional Application 60/257,624. Applicants attach hereto copies of the following items:

- 1) a copy of a preliminary amendment that was separately filed with the United States Patent Office on even date herewith that deletes this priority claim from the specification;
- 2) a copy of the declaration filed by the inventors on October 12, 2001, indicating that no such priority claim had been made by the inventors; and
- 3) a marked up copy of the filing receipt.

Applicants note that the specification erroneously contained a priority claim to United States Provisional Application 60/257,624 in the first sentence of the specification. However, this priority claim was unintentional. Applicants' declaration contained no such priority claim. Accordingly, Applicants have separately filed a preliminary amendment that deletes any reference of this priority claim from the specification. It is believed that no fee is required for this Request. However, should a fee be required, please charge the required fee to Pennie & Edmonds' Deposit Account No. 16-1150. This form is submitted in duplicate for such purpose.

Respectfully submitted,

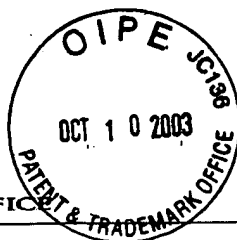
Date: October 10, 2003

  
\_\_\_\_\_  
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31,066  
(Reg. No.)



UNITED STATES  
PATENT AND  
TRADEMARK OFFICE



Commissioner for Patents  
Washington, DC 20231  
www.uspto.gov

APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/839,637 ✓	04/20/2001 ✓	2822 ✓	1134 ✓	M-8915 US ✓	35	93 ✓	4 ✓

11090-33-099

CONFIRMATION NO. 2154 ✓

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**CORRECTED FILING RECEIPT**



\*OC00000009392845\*

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RECORDING & DOCKETING CA OFFICE

Date Mailed: 01/17/2003

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. **If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).**

**Applicant(s)**

Mohammad H.S. Amin, Vancouver, CANADA; ✓  
Timothy Duty, Vancouver, CA; ✓  
Alexander Omelyanchouk, Khaira, UKRAINE; ✓  
Geordie Rose, Vancouver, CANADA; ✓  
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Alexandre Blais, Sherbrooke, CANADA; ✓

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**Domestic Priority data as claimed by applicant**

~~This appln claims benefit of 60/267,624 12/22/2000~~

**Foreign Applications**

If Required, Foreign Filing License Granted: 06/14/2001

Projected Publication Date: Not Applicable

Non-Publication Request: No

Early Publication Request: No

**\*\* SMALL ENTITY \*\*** ✓

Title

Quantum bit with a multi-ferminal junction and loop with a phase shift

**Preliminary Class**



**LICENSE FOR FOREIGN FILING UNDER  
Title 35, United States Code, Section 184  
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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**NOT GRANTED**

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